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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/757,115	01/14/2004	Steven O. Smith	24317/82951	2949	
7590 05/22/2006			EXAMINER		
Philip W. Woo			NGUYEN, HIEP		
SIDLEY AUSTIN BROWN & WOOD LLP Suite 5000			ART UNIT	PAPER NUMBER	
555 California Street			2816		
San Francisco, CA 94104-1715			DATE MAILED: 05/22/2006		

Please find below and/or attached an Office communication concerning this application or proceeding.

		_	_	- t-(:
		Application No.	Applicant(s)	
		10/757,115	SMITH ET AL.	
	Office Action Summary	Examiner	Art Unit	
		Hiep Nguyen	2816	
Period fo	The MAILING DATE of this communication apport	pears on the cover sheet with the	correspondence address	
A SH WHIC - Exte after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REPL CHEVER IS LONGER, FROM THE MAILING D nsions of time may be available under the provisions of 37 CFR 1.1 SIX (6) MONTHS from the mailing date of this communication. Decriod for reply is specified above, the maximum statutory period re to reply within the set or extended period for reply will, by statute reply received by the Office later than three months after the mailin ed patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION (36(a)). In no event, however, may a reply be the will apply and will expire SIX (6) MONTHS from the cause the application to become ABANDON	N. imely filed in the mailing date of this communication. ED (35 U.S.C. § 133).	
Status				
2a)	Responsive to communication(s) filed on <u>21 M</u> This action is <b>FINAL</b> . 2b) This Since this application is in condition for alloward closed in accordance with the practice under the	s action is non-final. nce except for formal matters, pr		
Dienoeit	ion of Claims			
5)⊠ 6)⊠ 7)□ 8)□	Claim(s) 2-16 and 18-25 is/are pending in the 4a) Of the above claim(s) is/are withdra Claim(s) 2-16 and 21-25 is/are allowed.  Claim(s) 18-20 is/are rejected.  Claim(s) is/are objected to.  Claim(s) are subject to restriction and/or ion Papers	wn from consideration.		
·· _	•			
10)	The specification is objected to by the Examine The drawing(s) filed on is/are: a) accomplicant may not request that any objection to the Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Example 2.	epted or b) objected to by the drawing(s) be held in abeyance. So tion is required if the drawing(s) is o	ee 37 CFR 1.85(a). bjected to. See 37 CFR 1.121(d).	
Priority (	under 35 U.S.C. § 119			
a)	Acknowledgment is made of a claim for foreign All b) Some * c) None of:  1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority application from the International Burea See the attached detailed Office action for a list	ts have been received. ts have been received in Applica rity documents have been receiv u (PCT Rule 17.2(a)).	tion No ved in this National Stage	
2) Notice 3) Inform	et(s) te of References Cited (PTO-892) te of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) or No(s)/Mail Date	4) Interview Summar Paper No(s)/Mail I 5) Notice of Informal 6) Other: <u>attached do</u>	Date Patent Application (PTO-152)	

Application/Control Number: 10/757,115

Art Unit: 2816

### **DETAILED ACTION**

## Claim Rejections - 35 USC § 112

Claims 18-20 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Correction and/or clarification is required.

Regarding claim 18, the recitation "<u>at most</u> three transistors operable to provide signal currents" on line 4 is indefinite because it is misdescriptive. Figure 1 of the present application shows that there are more than 3 transistors (12, 14, 16, 18, 20, 22) in the circuit that provide signal currents.

Claims 19 and 20 are indefinite because of the technical deficiencies of claim 18.

# Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 18-20 are rejected under 35 U.S.C. 102(b) as being anticipated by Xu (US 6,066,985). See attached paper.

Regarding claims 18-20, figure 2 of Xu shows a buffer circuit comprising an input terminal (INPUT), an output terminal (OUTP) three transistors (N105, N106, P117) wherein the output (OUTP) is feedback to two matched transistors (N105, N106), first and second current sources (P107, P108). Note that two transistors (N105, N106) are considered to be identical (matched) otherwise stated. When the input voltage decreases, the output voltage (OUTP) decreases (see abstract); transistor (N105) is turned on less, the input voltage (INPUT) increases thus the input voltage change is countered. The first and second matched current sources are transistors (P107) and (P108).

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#### Allowable Subject Matter

Claims 2-16 and 21-25 are allowed because the prior art of records (US 6,066,985) fails to teach or suggest a buffer circuit comprising a third transistor having the source coupled the source coupled to the source of the second transistor and the gate coupled to the drain of first transistor as called for in claims 2, 7, 9, 10 and 21.

#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hiep Nguyen whose telephone number is (571) 272-1752. The examiner can normally be reached on Monday to Friday from 7:30am to 4:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on (571) 272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Hiep Nguyen

05-16-06

PRIMARY EXAMINER